

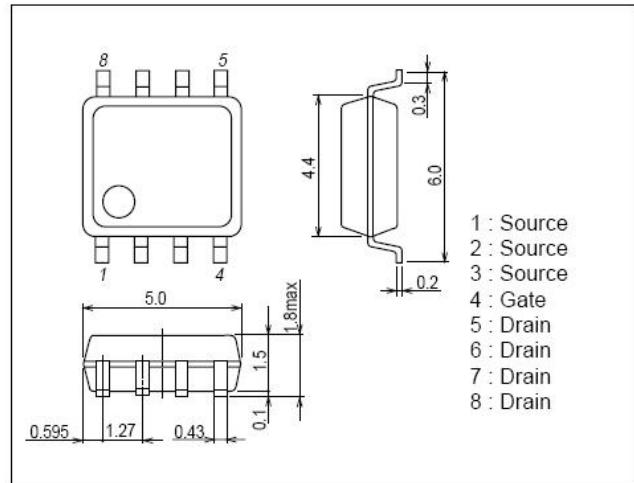
## Features

- Low On resistance.
- -4.5V drive.
- RoHS compliant.



## Package Dimensions

unit : mm  
SOP-8



## Specifications

### Absolute Maximum Ratings at $T_a=25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	$V_{DSS}$		-30	V
Gate-to-Source Voltage	$V_{GSS}$		$\pm 20$	V
Drain Current (DC)	$I_D$		-10	A
Drain Current (Pulse)	$I_{DP}$	$PW \leq 10\mu\text{s}$ , duty cycle $\leq 1\%$	-40	A
Allowable Power Dissipation	$P_D$	Mounted on a ceramic board ( $1000\text{mm}^2 \times 0.8\text{mm}$ ) 1unit	3.2	W
Total Dissipation	$P_T$	Mounted on a ceramic board ( $1000\text{mm}^2 \times 0.8\text{mm}$ )	-	W
Channel Temperature	$T_{ch}$		150	$^\circ\text{C}$
Storage Temperature	$T_{stg}$		-55~+150	$^\circ\text{C}$

### Electrical Characteristics at $T_a=25^\circ\text{C}$

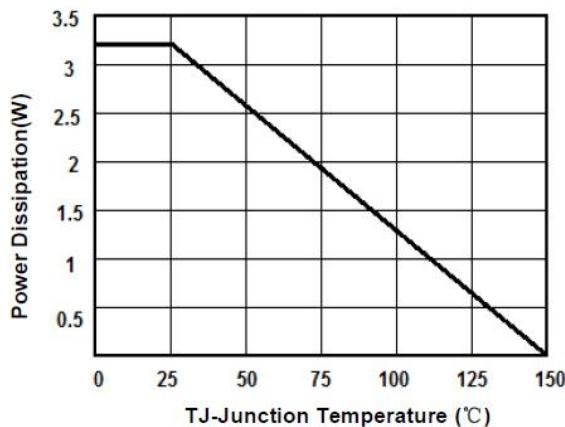
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D=-250\mu\text{A}$ , $V_{GS}=0\text{V}$	-30			V
Zero-Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=-24\text{V}$ , $V_{GS}=0\text{V}$			-10	$\mu\text{A}$
Gate-to-Source Leakage Current	$I_{GSS}$	$V_{GS}=\pm 20\text{V}$ , $V_{DS}=0\text{V}$			$\pm 100$	nA
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS}=V_{GS}$ , $I_D=-250\mu\text{A}$	-1	-1.6	-3.0	V
Forward Transconductance	$g_{FS}$	$V_{DS}=-5\text{V}$ , $I_D=-5\text{A}$		18		S
Static Drain-to-Source On-State Resistance	$R_{DS(\text{ON})}$	$I_D=-10\text{A}$ , $V_{GS}=-10\text{V}$		13	20	$\text{m}\Omega$
	$R_{DS(\text{ON})}$	$I_D=-5\text{A}$ , $V_{GS}=-4.5\text{V}$		19	30	$\text{m}\Omega$
Input Capacitance	$C_{iss}$	$V_{DS}=-15\text{V}$ , $V_{GS}=0\text{V}$ , $f=1\text{MHz}$		1800	-	pF
Output Capacitance	$C_{oss}$	$V_{DS}=-15\text{V}$ , $V_{GS}=0\text{V}$ , $f=1\text{MHz}$		305		pF
Reverse Transfer Capacitance	$C_{rss}$	$V_{DS}=-15\text{V}$ , $V_{GS}=0\text{V}$ , $f=1\text{MHz}$		216		pF

## Electrical Characteristics at $T_a=25^{\circ}\text{C}$ (Continued)

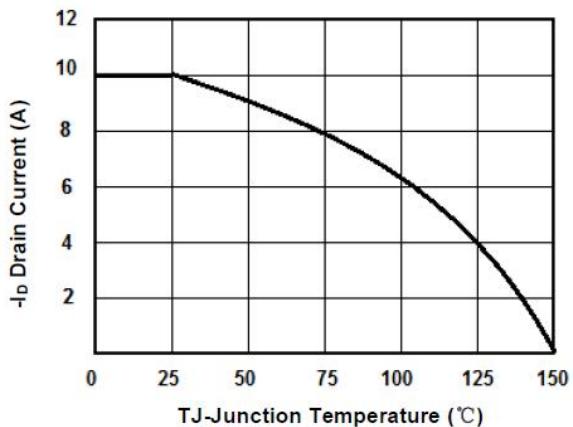
Parameter	Symbol	Conditions	Ratings			Unit
			min	Typ	max	
Turn-on Delay Time	$t_{d(on)}$	$V_{GS}=-10\text{V}, V_{DS}=-15\text{V}, R_L=15\Omega,$ $R_{GEN}=2.5\Omega$		10		nS
Rise Time	$t_r$			26		nS
Turn-off Delay Time	$t_{d(off)}$			35		nS
Fall Time	$t_f$			8		nS
Total Gate Charge	$Q_g$			30	-	nC
Gate-to-Source Charge	$Q_{gs}$			6		nC
Gate-to-Drain "Miller" Charge	$Q_{gd}$	$V_{DS}=-10\text{V}, V_{GS}=-15\text{V}, I_D=-1\text{A}$		9		nC
Diode Forward Voltage	$V_{SD}$		$I_S=-10\text{A}, V_{GS}=0\text{V}$		-	-1.2 V

## Typical Characteristics at $T_a=25^{\circ}\text{C}$

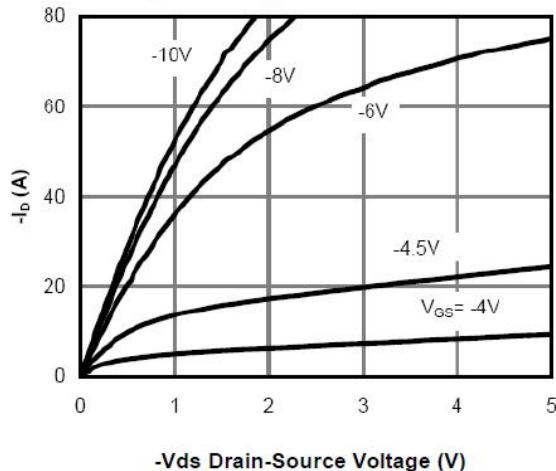
**Figure1. Power Dissipation**



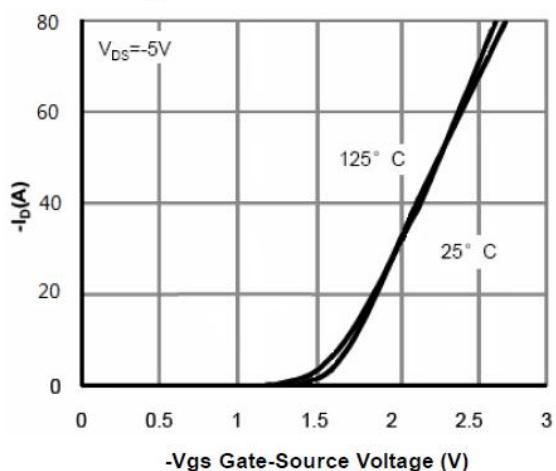
**Figure2. Drain Current**



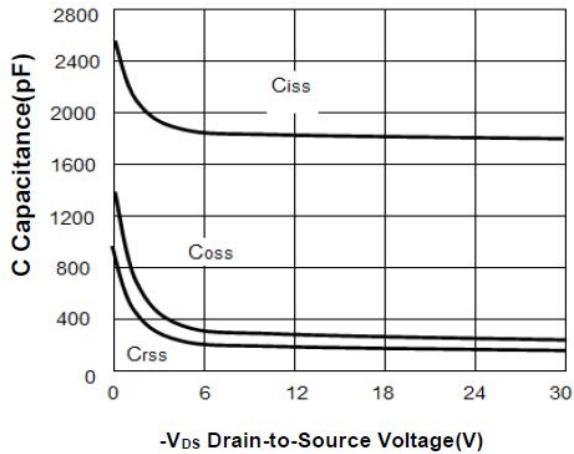
**Figure3. Output Characteristics**



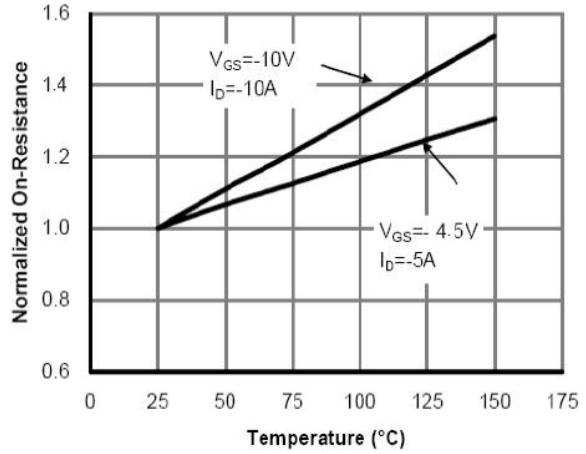
**Figure4. Transfer Characteristics**



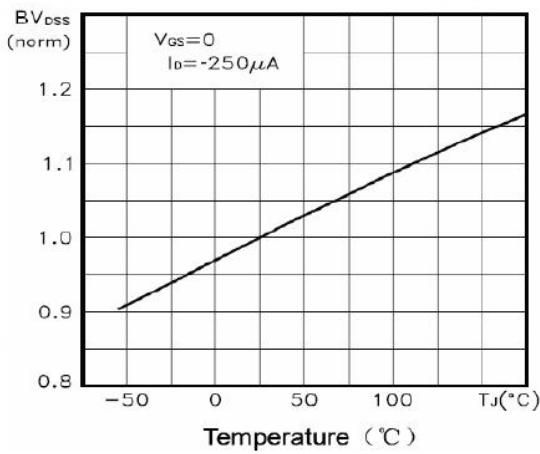
**Figure5. Capacitance**



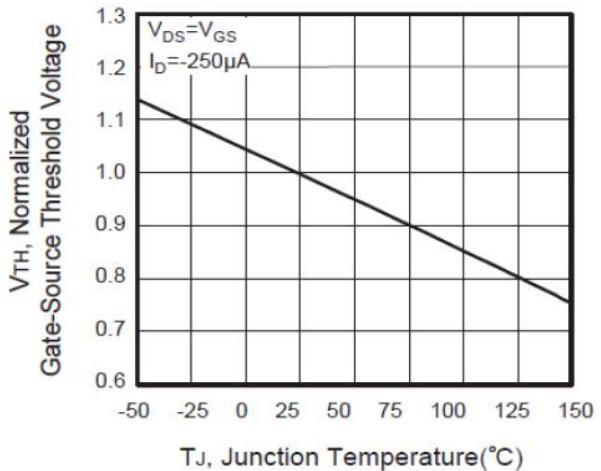
**Figure6. R<sub>DSON</sub> vs Junction Temperature**



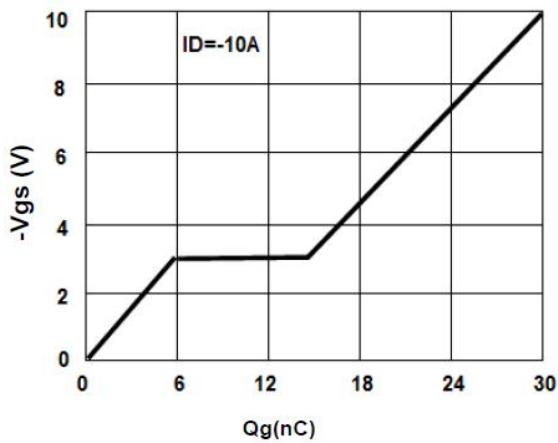
**Figure7. Max BV<sub>DSS</sub> vs Junction Temperature**



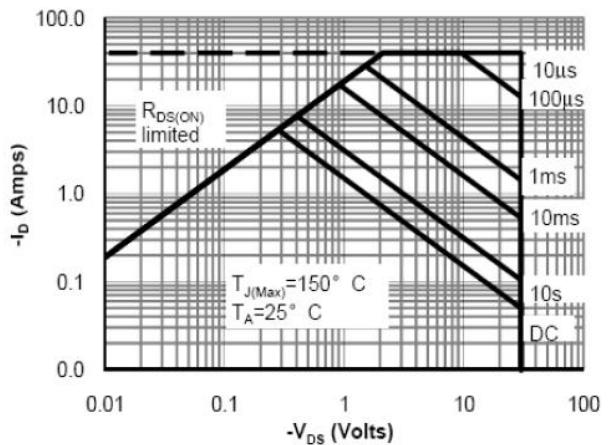
**Figure8. V<sub>GS(th)</sub> vs Junction Temperature**



**Figure9. Gate Charge Waveforms**



**Figure10. Maximum Safe Operating Area**



**Figure11. Normalized Maximum Transient Thermal Impedance**

